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January 3, 2005

TO: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Art Unit 2824 - Examiner Menz, Douglas M

FROM: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

SUBJECT: Serial #: 10/614,928
File Date: 07/08/2003
Inventor: Jin-Yuan Lee
Examiner: Menz, Douglas M
Art Unit: 2824
Title: A Structure of High Performance Combo Chip and Processing Method

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement in the Office Action dated Dec. 1, 2004. In that office action, restriction was required to one of the following Inventions under

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents P.O. Box 1450, Alexandria, VA 22313-1450 on Jan. 3, 2005.

Signature: 
Stephen B. Ackerman, Reg. No. 37,761

Date: Jan. 3, 2005